

SN54HC352, SN74HC352 DUAL 4-LINE TO 1-LINE DATA SELECTORS/MUXPLEXERS

D2684, DECEMBER 1982—REVISED SEPTEMBER 1987

- Inverting Versions of 'HC153
- High-Current Inverting Outputs Can Drive Up to 15 LSTTL Loads
- Permits Multiplexing from n Lines to 1 Line
- Performs Parallel-to-Serial Conversion
- Strobe (Enable) Line Provided for Cascading (N Lines to n Lines)
- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs
- Dependable Texas Instruments Quality and Reliability

description

Separate output enable inputs (\bar{G}) are provided for each of the two four-line sections of these data selectors/multiplexers.

The SN54HC352 is characterized for operation over the full military temperature range of -55°C to 125°C . The SN74HC352 is characterized for operation from -40°C to 85°C .

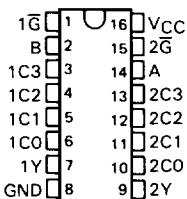
FUNCTION TABLE

SELECT INPUTS	DATA INPUTS				OUTPUT ENABLE	OUTPUT	
	B	A	C0	C1	C2	C3	
X X	X	X	X	X	X	H	H
L L	L	L	X	X	X	L	H
L L	H	H	X	X	X	L	L
L H	X	L	X	X	X	L	H
L H	X	H	X	X	X	L	L
H L	X	X	L	X	X	L	H
H L	X	X	H	X	X	L	L
H H	X	X	X	L	X	L	H
H H	X	X	X	H	X	L	L

Select inputs A and B are common to both sections.

SN54HC352 . . . J PACKAGE
SN74HC352 . . . DW OR N PACKAGE

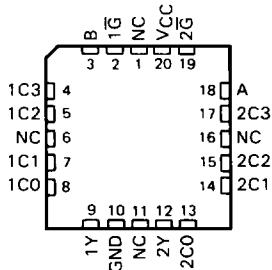
(TOP VIEW)



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SN54HC352 . . . FK PACKAGE

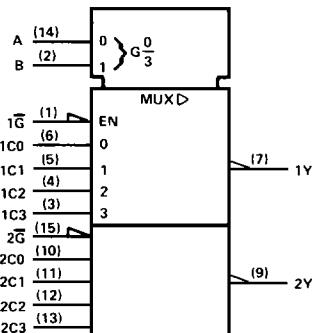
(TOP VIEW)



HCMOS Devices

NC—No internal connection

logic symbol†



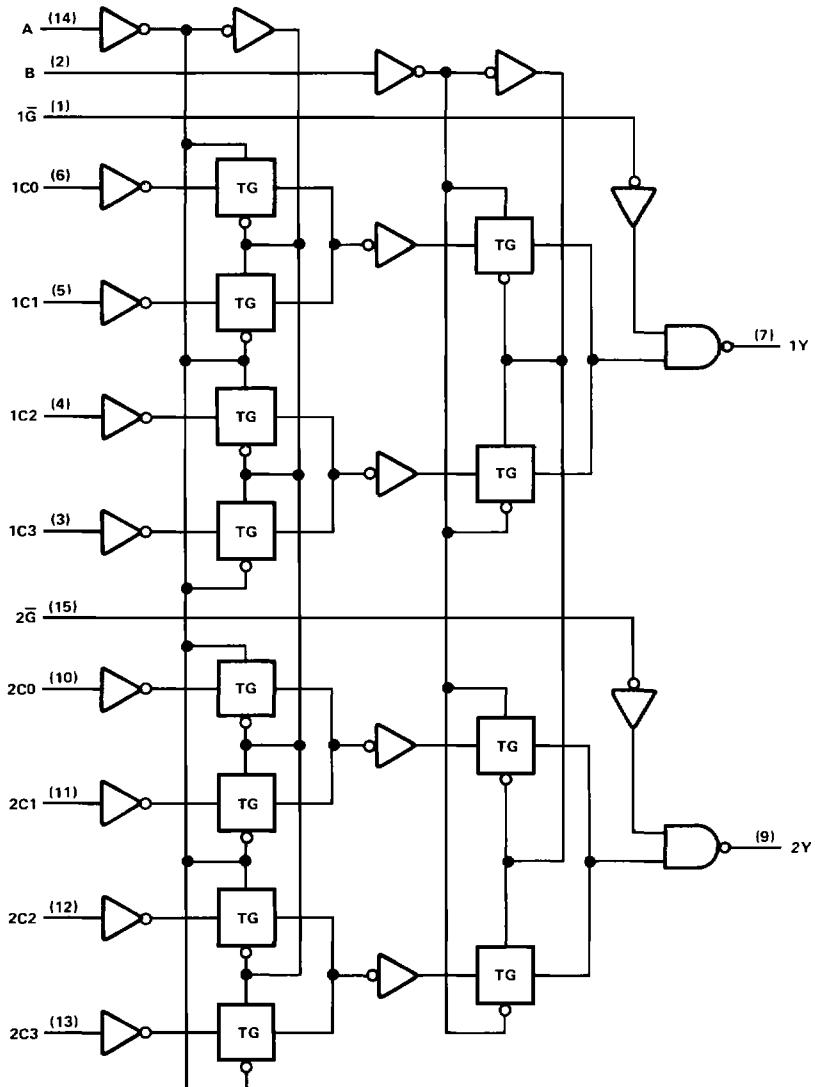
†This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.
Pin numbers shown are for DW, J, and N packages.

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DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

logic diagram (positive logic)

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HCMOS Devices



Pin numbers shown are for DW, J, and N packages.

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DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

absolute maximum ratings over operating free-air temperature range[†]

Supply voltage, V _{CC}	-0.5 V to 7 V
Input clamp current, I _{IK} (V _I < 0 or V _I > V _{CC})	±20 mA
Output clamp current, I _{OK} (V _O < 0 or V _O > V _{CC})	±20 mA
Continuous output current, I _O (V _O = 0 to V _{CC})	±35 mA
Continuous current through V _{CC} or GND pins	±70 mA
Lead temperature 1.6 mm (1/16 in) from case for 60 s: FK or J package	300°C
Lead temperature 1.6 mm (1/16 in) from case for 10 s: DW or N package	260°C
Storage temperature range	-65°C to 150°C

[†]Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN54HC352			SN74HC352			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC}	Supply voltage	2	5	6	2	5	6	V
V _{IH}	High-level input voltage	V _{CC} = 2 V	1.5		1.5			V
		V _{CC} = 4.5 V	3.15		3.15			
		V _{CC} = 6 V	4.2		4.2			
V _{IL}	Low-level input voltage	V _{CC} = 2 V	0	0.3	0	0.3		V
		V _{CC} = 4.5 V	0	0.9	0	0.9		
		V _{CC} = 6 V	0	1.2	0	1.2		
V _I	Input voltage	0	V _{CC}		0	V _{CC}		V
V _O	Output voltage	0	V _{CC}		0	V _{CC}		V
t _t	Input transition (rise and fall) times	V _{CC} = 2 V	0	1000	0	1000		ns
		V _{CC} = 4.5 V	0	500	0	500		
		V _{CC} = 6 V	0	400	0	400		
T _A	Operating free-air temperature	-55	125	-40	85		85	°C

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DUAL 4-LINE TO 1-LINE DATA SELECTORS/MUXES

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	V _{CC}	T _A = 25°C			SN54HC352		SN74HC352		UNIT
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
V _{OH}	V _I = V _{IH} or V _{IL} , I _{OH} = -20 μA	2 V	1.9	1.998		1.9		1.9		V
		4.5 V	4.4	4.499		4.4		4.4		
		6 V	5.9	5.999		5.9		5.9		
	V _I = V _{IH} or V _{IL} , I _{OH} = -6 mA	4.5 V	3.98	4.30		3.7		3.84		V
	V _I = V _{IH} or V _{IL} , I _{OH} = -7.8 mA	6 V	5.48	5.80		5.2		5.34		
V _{OL}	V _I = V _{IH} or V _{IL} , I _{OL} = 20 μA	2 V	0.002	0.1		0.1		0.1		V
		4.5 V	0.001	0.1		0.1		0.1		
		6 V	0.001	0.1		0.1		0.1		V
	V _I = V _{IH} or V _{IL} , V _{OL} = 6 mA	4.5 V	0.17	0.26		0.4		0.33		
I _I	V _I = V _{CC} or 0	6 V	0.15	0.26		0.4		0.33		nA
I _{OZ}	V _O = V _{CC} or 0	6 V	±0.01	±0.5		±10		±5		μA
I _{CC}	V _I = V _{CC} or 0, I _O = 0	6 V		8		160		80		μA
C _i		2 to 6 V		3	10		10		10	pF

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DUAL 4-LINE TO 1-LINE DATA SELECTORS/MUXES

switching characteristics over recommended operating free-air temperature range (unless otherwise noted), $C_L = 50 \text{ pF}$ (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC}	T _A = 25°C			SN54HC352		SN74HC352		UNIT
				MIN	Typ	MAX	MIN	MAX	MIN	MAX	
t_{pd}	A or B	Y	2 V	58	185	280			230		
			4.5 V	17	37	56			46		
			6 V	14	32	48			39		
t_{pd}	Data (Any C)	Y	2 V	47	175	265			220		
			4.5 V	14	35	53			44		
			6 V	12	30	45			37		
t_{pd}	\overline{G}	Y	2 V	27	135	205			170		
			4.5 V	10	27	41			34		
			6 V	8	23	35			29		
t_t		Y	2 V	20	60	90			75		
			4.5 V	8	12	18			15		
			6 V	6	10	15			13		

C _{pd}	Power dissipation capacitance per data selector	No load, T _A = 25°C	40 pF typ
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switching characteristics over recommended operating free-air temperature range (unless otherwise noted), $C_L = 150 \text{ pF}$ (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC}	T _A = 25°C			SN54HC352		SN74HC352		UNIT
				MIN	Typ	MAX	MIN	MAX	MIN	MAX	
t_{pd}	A or B	Y	2 V	72	270	410			335		
			4.5 V	22	54	82			67		
			6 V	19	47	70			58		
t_{pd}	Data (Any C)	Y	2 V	62	260	395			325		
			4.5 V	19	52	79			63		
			6 V	16	45	67			56		
t_{pd}	\overline{G}	Y	2 V	43	220	335			275		
			4.5 V	14	44	67			55		
			6 V	12	38	57			48		
t_t		Y	2 V	45	210	315			265		
			4.5 V	17	42	63			53		
			6 V	13	36	53			45		

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.

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HCmos Devices

TEXAS
INSTRUMENTS

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